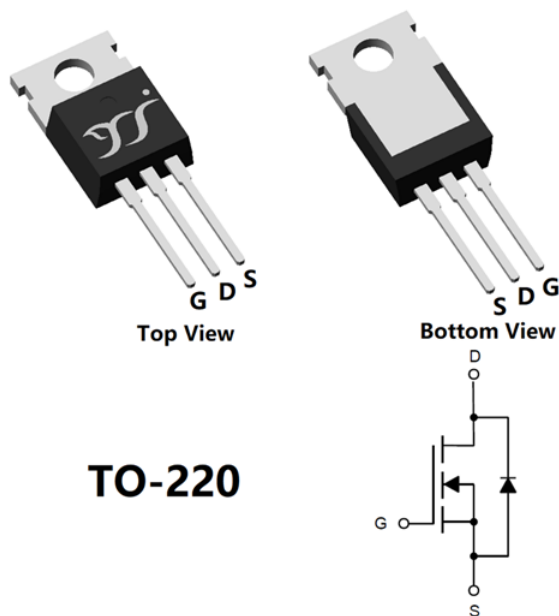


N-Channel Enhancement Mode Field Effect Transistor



Product Summary

- V_{DS} 650V
- I_D 15A
- $R_{DS(ON)}$ (at $V_{GS}=10V$) $<260m\Omega$
- 100% EAS Tested
- 100% ∇V_{DS} Tested

General Description

- Super Junction High Voltage MOSFET technology
- Excellent package for heat dissipation
- High density cell design for low $R_{DS(ON)}$
- Epoxy Meets UL 94 V-0 Flammability Rating
- Halogen Free

Applications

- Switching Mode Power Supplies (SMPS)
- PWM Motor Controls
- LED Lighting
- Adapter

Limiting Values

Parameter	Conditions		Symbol	Min	Max	Unit
Drain-source Voltage			V_{DS}	-	650	V
Gate-source Voltage			V_{GS}	-30	30	
Continuous Drain Current (Note 1,2)	Steady-State	$T_A=25^{\circ}C, V_{GS}=10V$	I_D	-	2.3	A
		$T_A=100^{\circ}C, V_{GS}=10V$		-	1.4	
Continuous Drain Current (Note 1,3)	Steady-State	$T_C=25^{\circ}C, V_{GS}=10V, \text{Chip limitation}$		-	15	
		$T_C=100^{\circ}C, V_{GS}=10V$		-	9.5	
Pulsed Drain Current	$T_C=25^{\circ}C, t_p \leq 10\mu s$		I_{DM}	-	45	
Maximum Body-Diode Continuous Current	$T_C=25^{\circ}C$		I_S		15	
Maximum Body-Diode Pulsed Current	$T_C=25^{\circ}C, t_p \leq 10\mu s$		I_{SM}	-	45	
Avalanche Energy (non-repetitive)	$T_J=25^{\circ}C, V_G=10V, R_G=25\Omega, L=30mH, I_{AS}=4.6A$		EAS	-	317.4	mJ
Total Power Dissipation (Note 1,2)	Steady-State	$T_A=25^{\circ}C$	P_D	-	3.6	W
		$T_A=100^{\circ}C$		-	1.4	
Total Power Dissipation (Note 1,3)	Steady-State	$T_C=25^{\circ}C$		-	148	
		$T_C=100^{\circ}C$		-	59	
MOSFET dv/dt Ruggedness	$V_{DS}=0 \dots 325V, I_D \leq 14A, R_g=0\Omega$		dv/dt	-	46	V/ns
Reverse Diode dv/dt	$V_{DS}=0 \dots 325V, I_D \leq 14A, di/dt=200A/\mu s$		dv/dt	-	8.3	
Maximum Diode Commutation Speed	$V_{DS}=0 \dots 325V, I_D \leq 14A, R_g=0\Omega$		dif/dt	-	4197	A/ μs
Junction and Storage Temperature Range			T_J, T_{STG}	-55	150	$^{\circ}C$

Thermal Resistance

Parameter	Symbol	Typ	Max	Units
Thermal Resistance Junction-to-Ambient (Note 2)	$R_{\theta JA}$	-	34	$^{\circ}C/W$
Thermal Resistance Junction-to-Case	$R_{\theta JC}$	-	0.84	

Ordering Information (Example)

PREFERRED P/N	PACKING CODE	Marking	MINIMUM PACKAGE(pcs)	INNER BOX QUANTITY(pcs)	OUTER CARTON QUANTITY(pcs)	DELIVERY MODE
YJP260C65BHJ	B1	YJP260C65BH	50	/	5000	Tube



YJP260C65BHJ

■ Electrical Characteristics

Parameter	Symbol	Conditions	Min	Typ	Max	Units
Static Parameter						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =250μA, T _J =25℃	650	-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =650V, V _{GS} =0V, T _J =25℃	-	-	1	μA
		V _{DS} =650V, V _{GS} =0V, T _J =150℃	-	-	100	
Gate-Source Leakage Current	I _{GSS}	V _{GS} =±30V, V _{DS} =0V, T _J =25℃	-	-	±100	nA
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250μA, T _J =25℃	3.3	4.1	4.9	V
Static Drain-Source On-Resistance	R _{DS(on)}	V _{GS} =10V, I _D =7.5A, T _J =25℃	-	213	260	mΩ
		V _{GS} =10V, I _D =7.5A, T _J =150℃	-	543	663	
Diode Forward Voltage	V _{SD}	I _S =15A, V _{GS} =0V, T _J =25℃	-	0.88	1.2	V
Gate Resistance	R _G	f=1MHz, T _J =25℃	-	4.7	-	Ω
Dynamic Parameters						
Input Capacitance	C _{iss}	V _{DS} =325V, V _{GS} =0V, f=1MHz, T _J =25℃	-	1210	-	pF
Output Capacitance	C _{oss}		-	33.5	-	
Reverse Transfer Capacitance	C _{rss}		-	3.8	-	
Effective Output capacitance, Energy Related	C _{o(er)}	V _{DS} =0...325V, V _{GS} =0V, f=1MHz, T _J =25℃	-	50	-	
Effective Output Capacitance, Time Related	C _{o(tr)}		-	250	-	
Switching Parameters						
Total Gate Charge	Q _g	V _{GS} =10V, V _{DS} =325V, I _D =15A, T _J =25℃	-	27.4	-	nC
Gate-Source Charge	Q _{gs}		-	7.9	-	
Gate-Drain Charge	Q _{gd}		-	12.2	-	
Reverse Recovery Charge	Q _{rr}	I _F =15A, di/dt=100A/μs, V _{GS} =0V, V _R =325V, T _J =25℃	-	4437	-	nC
Reverse Recovery Time	t _{rr}		-	323	-	ns
Peak Reverse Recovery Current	I _{rm}		-	26	-	A
Turn-on Delay Time	t _{D(on)}	V _{GS} =10V, V _{DS} =325V, I _D =15A, R _{GEN} =3Ω, T _J =25℃	-	39.4	-	ns
Turn-on Rise Time	t _r		-	32.5	-	
Turn-off Delay Time	t _{D(off)}		-	24	-	
Turn-off Fall Time	t _f		-	12.4	-	

Note:

- The entire application environment impacts the thermal resistance values shown, they are not constants and are only valid for the particular conditions noted.
- The value of $R_{\theta JA}$ is measured with the device mounted on the 40mm*40mm*1.1mm single layer FR-4 PCB board with 1 in² pad of 2oz. Copper, in the still air environment with $T_A=25^\circ C$. The maximum allowed junction temperature of $150^\circ C$. The value in any given application depends on the user's specific board design.
- Thermal resistance from junction to soldering point (on the exposed drain pad).



■ Typical Electrical and Thermal Characteristics Diagrams

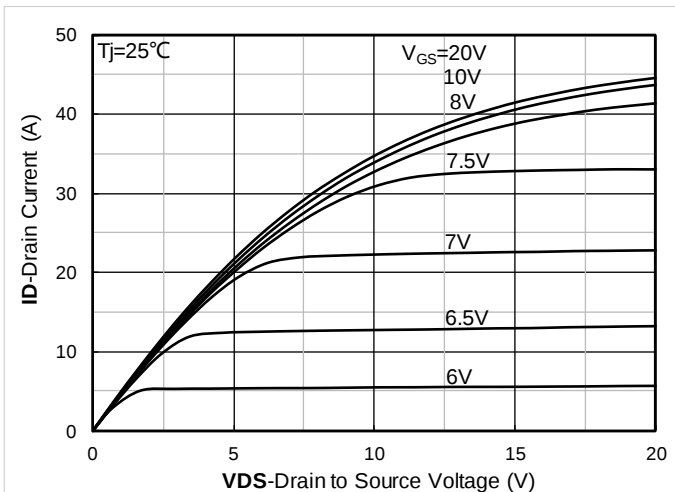


Figure 1. Output Characteristics; typical values

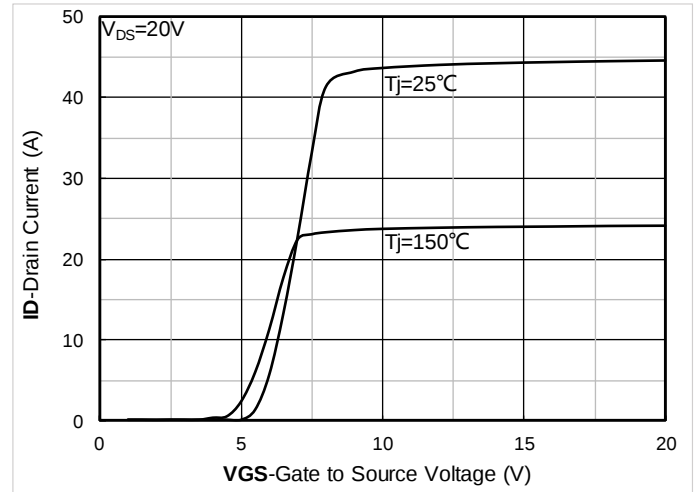


Figure 2. Transfer Characteristics; typical values

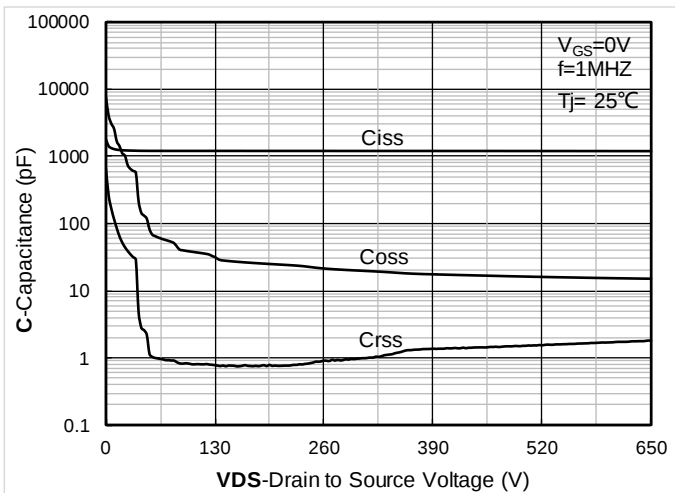


Figure 3. Capacitance Characteristics; typical values

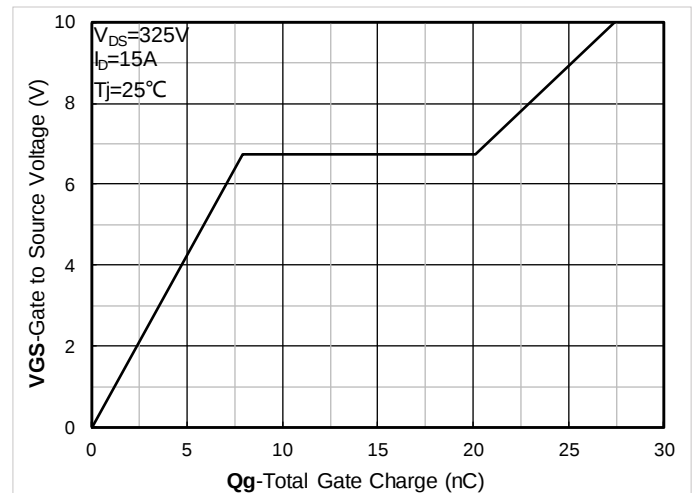


Figure 4. Gate Charge; typical values

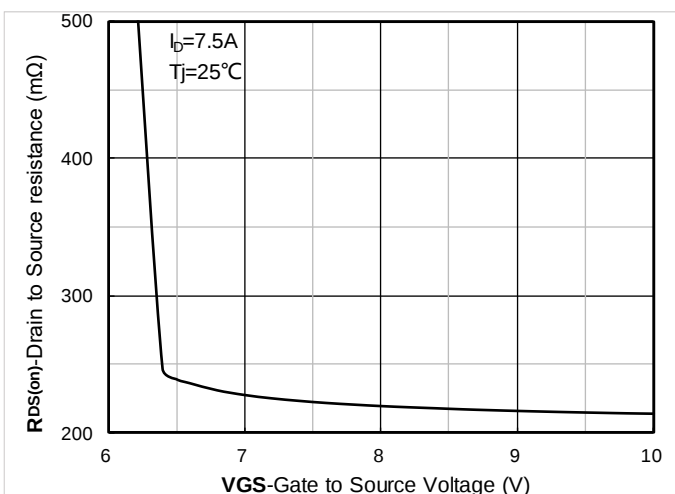


Figure 5. On-Resistance vs. Gate to Source Voltage; typical values

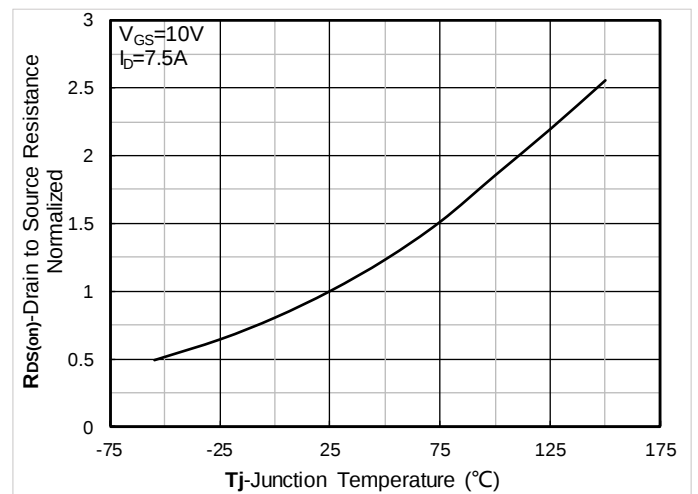


Figure 6. Normalized On-Resistance



YJP260C65BHJ

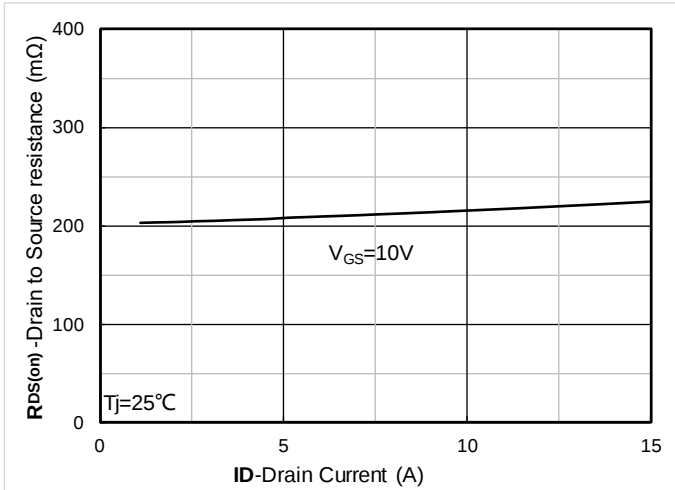


Figure 7. RDS(on) vs. Drain Current; typical values

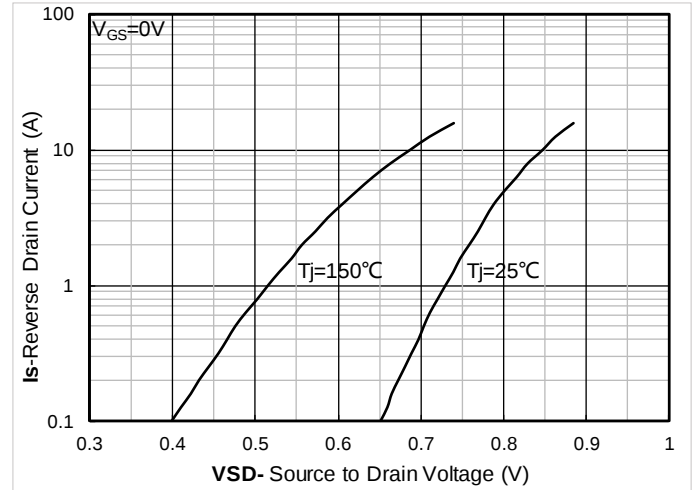


Figure 8. Forward characteristics of reverse diode; typical values

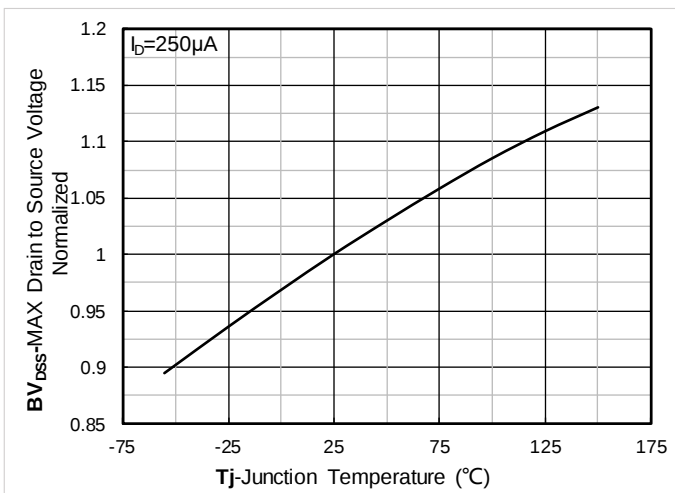


Figure 9. Normalized breakdown voltage

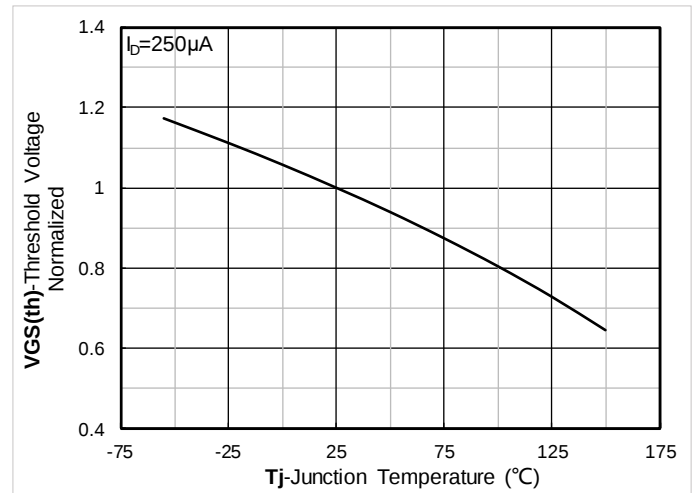


Figure 10. Normalized Threshold voltage

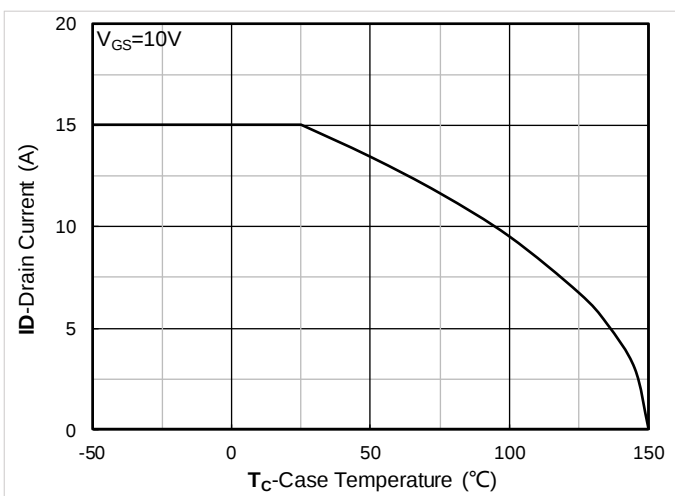


Figure 11. Current dissipation

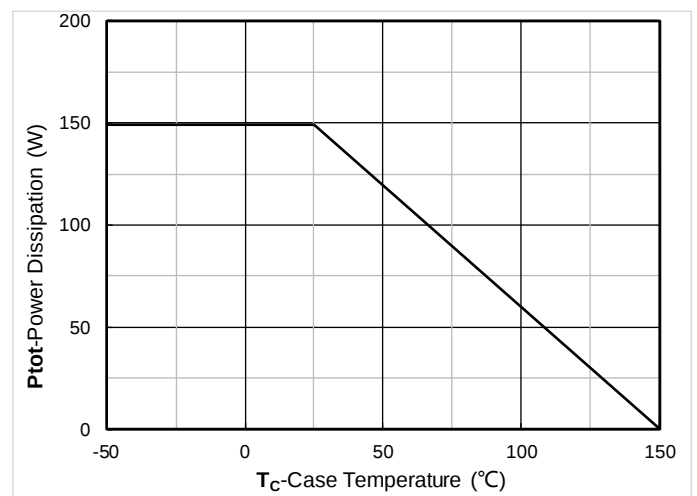


Figure 12. Power dissipation

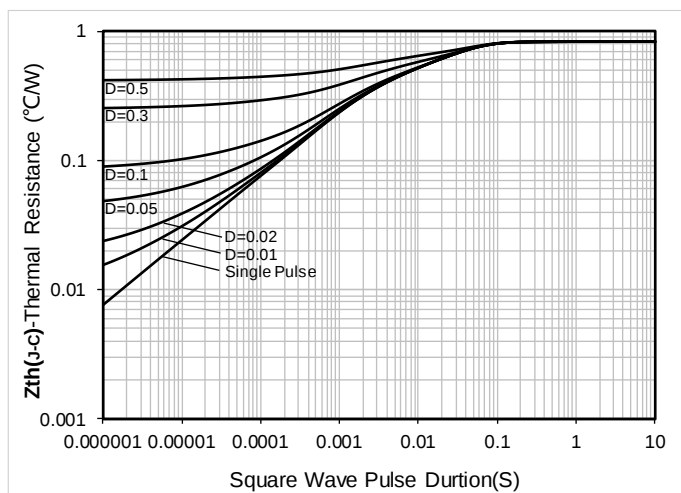


Figure 13. Maximum Transient Thermal Impedance

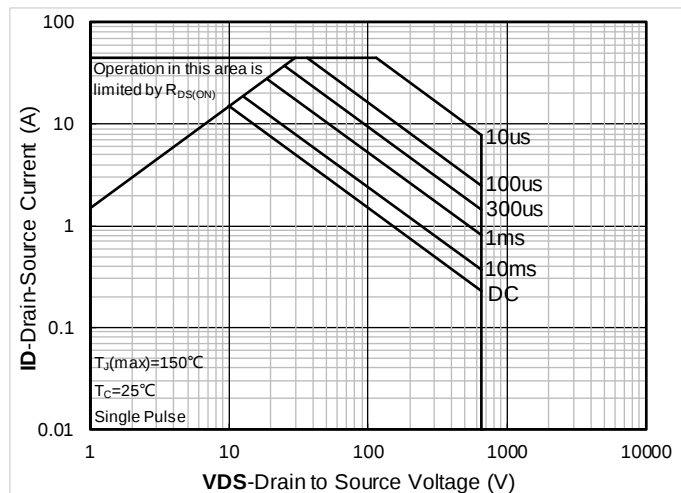


Figure 14. Safe Operation Area

■ Test Circuits & Waveforms

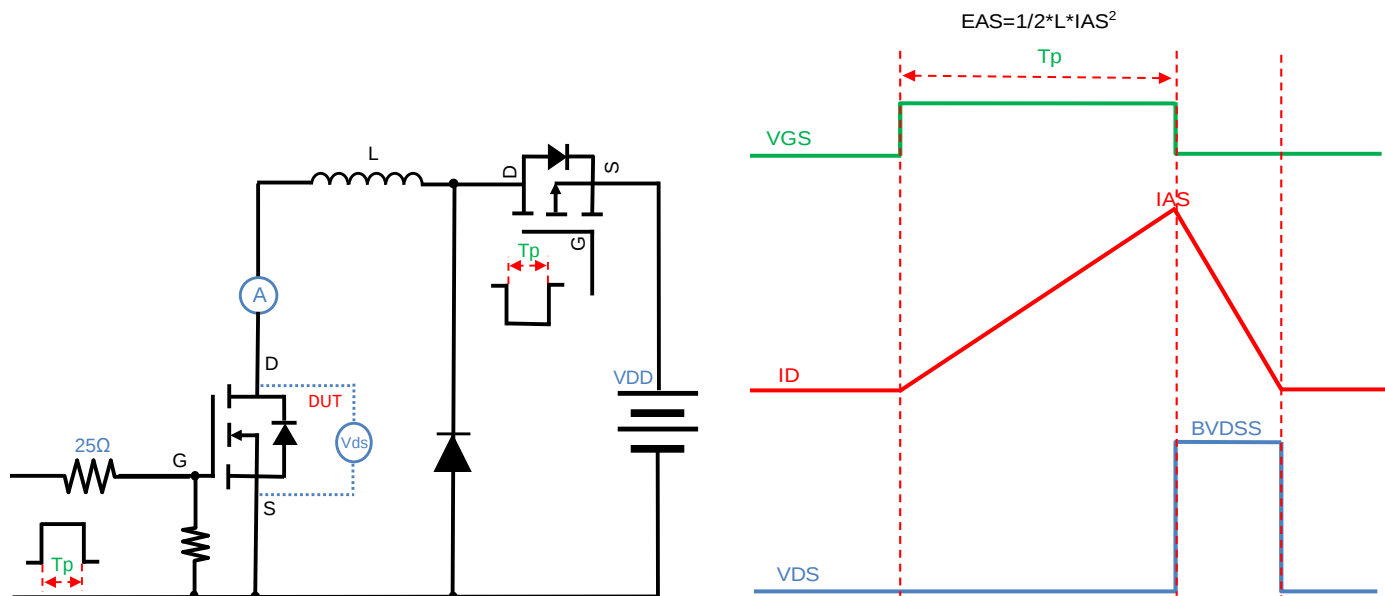


Figure A. Unclamped Inductive Switching (UIS) Test Circuit & Waveform

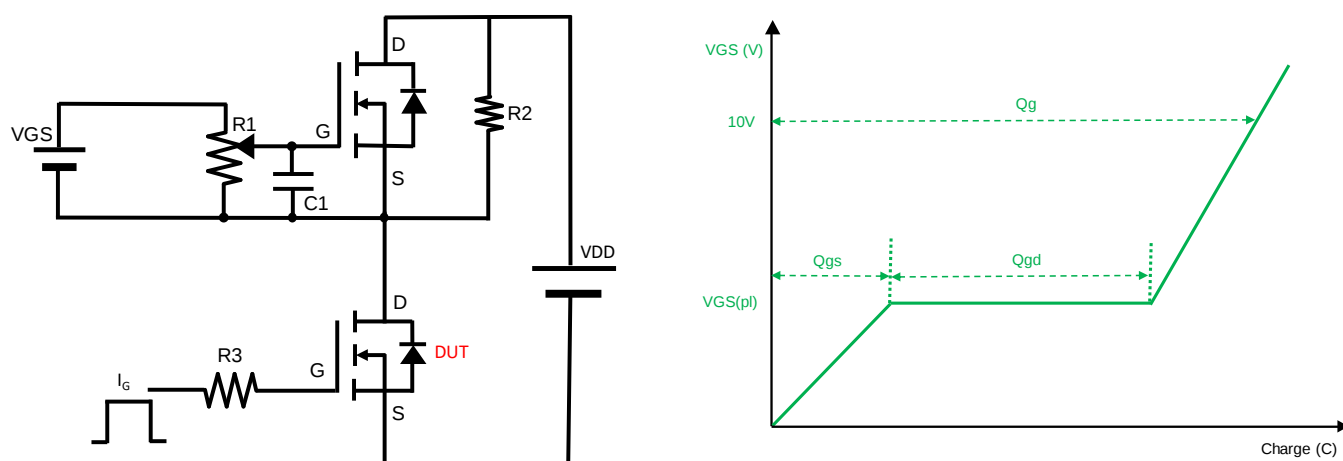


Figure B. Gate Charge Test Circuit & Waveform

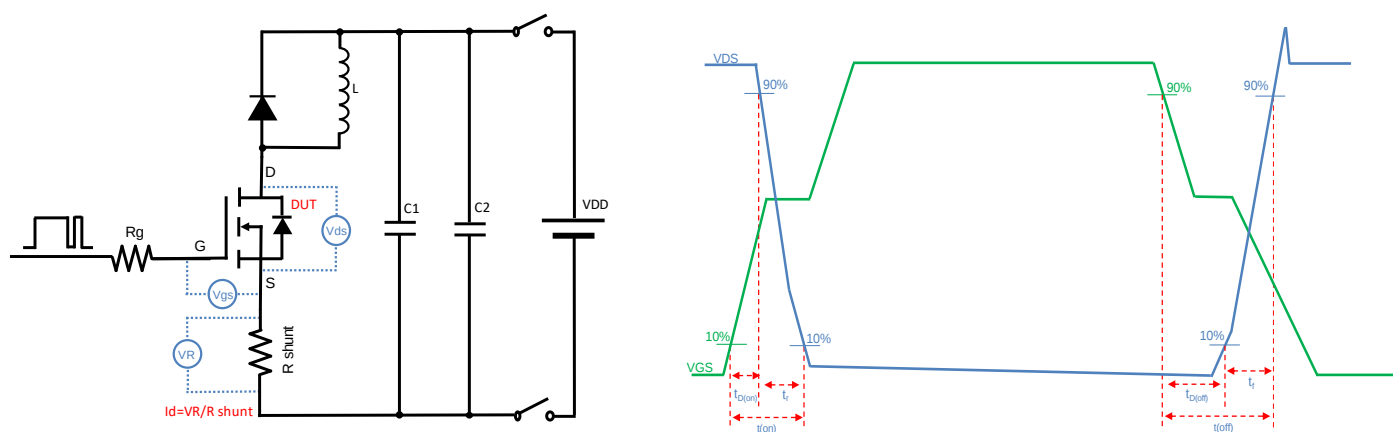


Figure C. Resistive Switching Test Circuit & Waveform

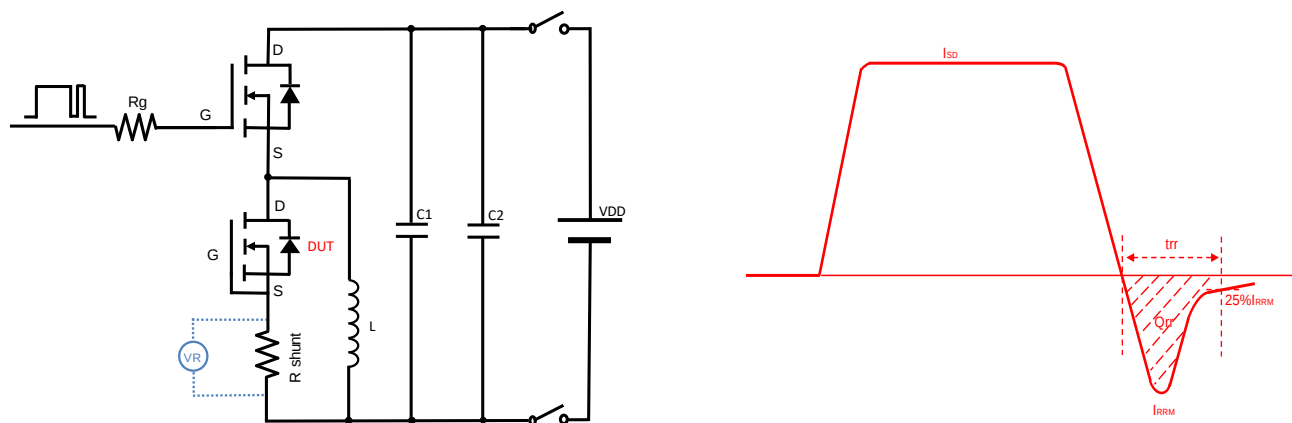
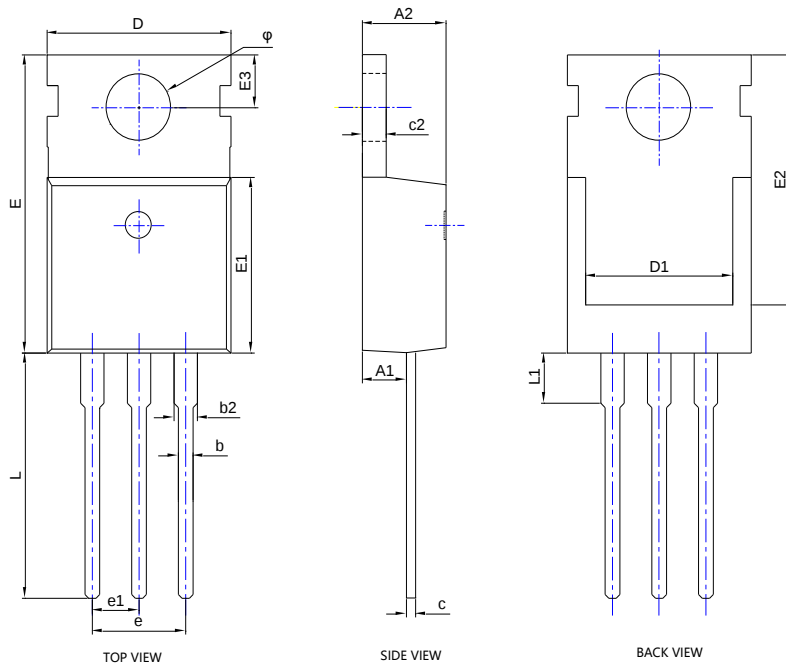


Figure D. Diode Recovery Test Circuit & Waveform



YJP260C65BHJ

■ TO-220AB-E Package information

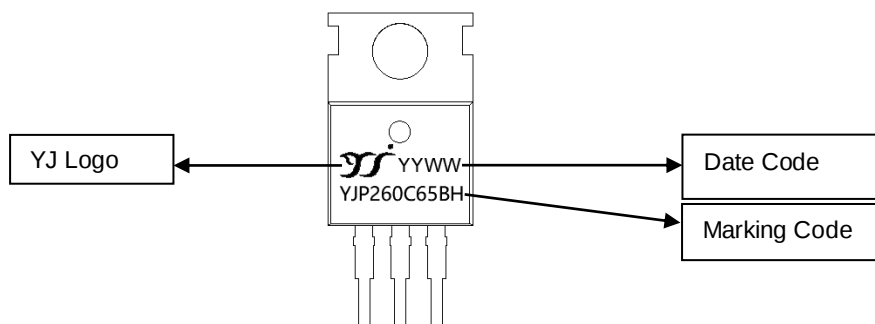


SYMBOL	DIMENSIONS			
	INCHES		Millimeter	
	MIN.	MAX.	MIN.	MAX.
A1	0.093	0.114	2.350	2.900
A2	0.176	0.184	4.470	4.670
b	0.028	0.036	0.710	0.910
b2	0.048	0.054	1.220	1.360
c	0.019	0.024	0.470	0.600
c2	0.047	0.055	1.200	1.400
D	0.382	0.408	9.700	10.370
D1	0.276	0.350	7.000	8.890
E	0.579	0.622	14.700	15.800
E1	0.350	0.373	8.900	9.470
E2	0.463	0.535	11.750	13.600
E3	0.108BSC		2.740BSC	
e	0.200BSC		5.080BSC	
e1	0.100BSC		2.540BSC	
L	0.508	0.583	12.900	14.800
L1	0.100	0.151	2.540	3.840
ϕ	0.142	0.154	3.600	3.900

NOTE:
1. PACKAGE BODY SIZES EXCLUDE MOLD FLASH AND GATE BURRS.
2. TOLERANCE 0.1mm UNLESS OTHERWISE SPECIFIED.



■ Marking Information



Note:

1. All marking is at middle of the product body
2. All marking is in laser printing
3. YJP260C65BH is marking code, YYWW is date code, "YY" is year, "WW" is week
4. Body color: Black



Disclaimer

The information presented in this document is for reference only. Yangzhou Yangjie Electronic Technology Co., Ltd. reserves the right to make changes without notice for the specification of the products displayed herein to improve reliability, function or design or otherwise.

The product listed herein is designed to be used with ordinary electronic equipment or devices, and not designed to be used with equipment or devices which require high level of reliability and the malfunction of which would directly endanger human life (such as medical instruments, transportation equipment, aerospace machinery, nuclear-reactor controllers, fuel controllers and other safety devices), Yangjie or anyone on its behalf, assumes no responsibility or liability for any damages resulting from such improper use of sale.

This publication supersedes & replaces all information previously supplied. For additional information, please visit our website <http://www.21yangjie.com> , or consult your nearest Yangjie's sales office for further assistance.